

Abstracts

An Easy-to-Use FET DRO Design Procedure Suited to Most CAD Programs

P.G. Wilson and R.D. Carver. "An Easy-to-Use FET DRO Design Procedure Suited to Most CAD Programs." 1989 MTT-S International Microwave Symposium Digest 89.3 (1989 Vol. III [MWSYM]): 1033-1036.

A design procedure for a reflection stabilised dielectric resonator oscillator (DRO) is given that takes advantage of the facilities available from most linear microwave CAD programs, hence streamlining and simplifying the task. 27.61GHz MESFET hybrid DROs were designed using this method. These DROs gave an average output power of +3dBm, with +/-2MHz stability from -20 to +40° C and -75dBc/Hz phase noise at 10 kHz from carrier.

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